

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

**INFORMATION DISCLOSURE STATEMENT**

APPLICANT(S): Masahiro Tada                      DOCKET NO.: 09792909-6091  
SERIAL NO.: 10/522,080                              GROUP ART UNIT: 2818  
DATE FILED: January 21, 2005                      EXAMINER: Thao P. Le  
INVENTION: SEMICONDUCTOR INTEGRATED DEVICE

Commissioner for Patents  
P. O. Box 1450  
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S I R:

In accordance with the provisions of 37 C.F.R. § 1.56, Applicants request that citation and examination of the references identified on the attached PTO-SB08A form, in accordance with 37 C.F.R. §1.98, be made during the course of examination of the above-referenced application for United States Letters Patent. Foreign Patent Document Cite Nos. 1 and 2 were cited in an Office Action issued on April 17, 2007, in connection with Japanese application no. JP2002-221433. U.S. Patent Application Publication Cite No. 1 and Non-Patent Literature Document Cite No. 1 were cited in a communication from the European Patent Office dated July 2, 2007. Copies of the foreign references and machine translations are attached as are the Japanese office action and the communication from the European Patent Office.

Respectfully submitted by,

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